

KSC2001

General Purpose Applications

High h_{FE} and Low V_{CE} (sat)



1. Emitter 2. Collector 3. Base

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a =25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	30	V
V _{CEO}	Collector-Emitter Voltage	25	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current	700	mA
I _B	Base Current	150	mA
P _C	Collector Dissipation	600	mW
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics T_a =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{BE} (on)	* Base Emitter On Voltage	V _{CE} =6V, I _C =10mA	600	640	700	mV
I _{CBO}	Collector Cut-off Current	V_{CB} =30V, I_{E} =0			100	nA
I _{EBO}	Emitter Cut-off Current	V_{EB} =5V, I_C =0			100	nA
h _{FE1}	* DC Current Gain	V _{CE} =1V, I _C =100mA	90	200	400	
h _{FE2}		V_{CE} =1V, I_{C} =700mA	50	140		
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C =700mA, I _B =70mA		0.2	0.6	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	I _C =700mA, I _B =70mA		0.95	1.2	V
C _{ob}	Output Capacitance	V _{CB} =6V, I _E =0, f=1MHz		13	25	pF
f _T	Current Gain Bandwidth Product	V _{CE} =6V, I _C =10mA	50	170		MHz

^{*} Pulse test: PW≤350µs, Duty cycle≤2%

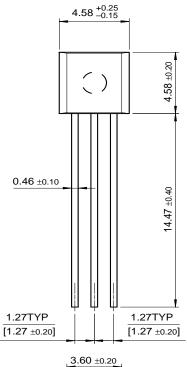
h_{FE1} Classification

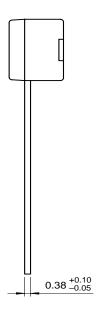
Classification	0	Y	G
h _{FE1}	90 ~ 180	135 ~ 270	200 ~ 400

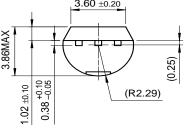
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Package Demensions

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